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Alexandria, VA 22313-1450**(571) 273-8300****From:** Edward B. Weller
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650-833-2436**Client-Matter Number:**

2102487-991181

Re: U.S. Patent Application No. 10/824,857**Pages:** - 6 - (including this form)**Originals:** ☐ will be mailed ☒ will not be mailed**If there is a problem with this transmission, please call (area code) (650) 833-5188**
Fax Operator/Ext.**Message:**Transmitted via Fax on: July 26, 2005
Attorney Docket No. 2102487-991181
U.S. Patent Application No. 10/824,857
Filing Date: April 14, 2004**Title: LAYOUT DESIGN SYSTEM, LAYOUT DESIGN METHOD AND LAYOUT DESIGN PROGRAM OF
SEMICONDUCTOR INTEGRATED CIRCUIT, AND METHOD OF MANUFACTURING THE SAME****Enclosed Is:** 1. Response to Office action of April 28, 2005**Certificate of Transmission by Facsimile**I hereby certify that this correspondence is being transmitted by facsimile to Fax No. (571) 273-8300 to:
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Maria Paula Kovacs**CONFIDENTIALITY NOTICE**

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(Form Rev. 6/5/00)

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JUL 26 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/824,857
Applicant : Mutsumori IGARASHI

Filed : April 14, 2004
TC/A.U. : 2825
Examiner : Tat, Binh C.
Title : **LAYOUT DESIGN SYSTEM, LAYOUT DESIGN METHOD
AND LAYOUT DESIGN PROGRAM OF SEMICONDUCTOR
INTEGRATED CIRCUIT, AND METHOD OF
MANUFACTURING THE SAME**

Docket No. : 2102487-991181
Customer No. : 26379

M/S Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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July 26, 2005.



Maria Paula Kovacs

RESPONSE TO OFFICE ACTION OF APRIL 28, 2005

Sir:

In response to the Office Action of April 28, 2005, please amend the above identified
application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this
paper.

replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 4 of this paper.

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